Seminar

Title: Research Results on Sigma Delta Modulator
Speaker: Professor Bosco Leung
University of Waterloo, Ontario, Canada
Time: 10:30am – 11:30am
Date: 8 June 2015 (Monday)
Venue: SHB 121

Abstract:
The talk focuses on research results on sigma delta modulator. This includes using sigma delta modulator in high IF digitizer, in I, Q path with mismatch suppressed, time based sigma delta modulator. There would be chip results in the presentation.

Biography:

Prof. Bosco H. Leung received the B.Sc. degree from Rensselaer Polytechnic Institute, Troy, in 1979, the M.Sc. degree from the California Institute of Technology, Pasadena, in 1980 and the Ph.D. degree from the University of California, Berkeley in 1987, all in electrical engineering. From 1980 to 1983, he was an analog circuit designer with Northern Telecom, Canada.


Prof. Leung was an associate editor for Communication Networks and RF Circuits for the IEEE Transactions on Circuits & Systems II: Analog & Digital Signal Processing.

*** ALL ARE WELCOME ***
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